

Application Serial No. 10/670,020
Reply to office action of December 28, 2006

PATENT
Docket: CU-3367

Amendments To The Claims

The listing of claims presented below will replace all prior versions, and listings, of claims in the application.

Listing of claims:

1. (currently amended) A device for driving an LCD comprising:

a timing control unit;

a gate driving unit operatively connected to said timing control unit and operatively connectable to a power supply circuit, the gate driving unit having a shift register, a level register and an output circuit, wherein the shift register, level register and the output circuit operatively interconnected together; and

a control signal transmission line operatively connected to the gate driving unit, the control signal transmission line for transmitting a data carry signal in which the data carry signal is used as an enable signal for each frame wherein the data carry signal is latched using the shift register, and the control signal transmission line for outputting an output control signal in which the output control signal is outputted after the data carry signal is latched, wherein a number of control signals required for driving the LCD is reduced whereby PCB design is simplified and signal interference phenomenon is reduced ~~or enabling the shift register and the control signal transmission line for transmitting an output control signal for controlling data output by the output circuit wherein the control signal transmission line is a single signal line, wherein the timing control unit controls the timing of the transmitted output control signal when the level register registers a level analog power supply voltage from the power supply~~

Application Serial No. 10/670,020
Reply to office action of December 28, 2006

PATENT
Docket: CU-3367

~~circuit and the timing control unit controls the timing of the transmitted output control signal by a delay of a load signal.~~

2. (previously presented) The device as claimed in claim 1, wherein the data carry signal uses a rising edge trigger system.

3. (original) The device as claimed in claim 1, wherein in order to prevent an overlapping of the data carry signal and the output control signal, the output control signal is outputted after one clock from a time point where the data carry signal is latched using the shift register.